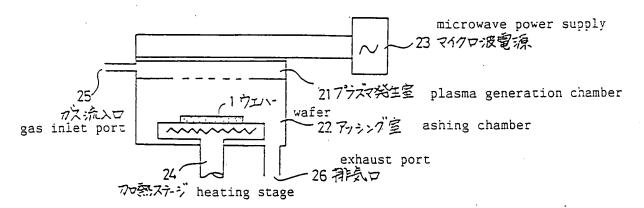
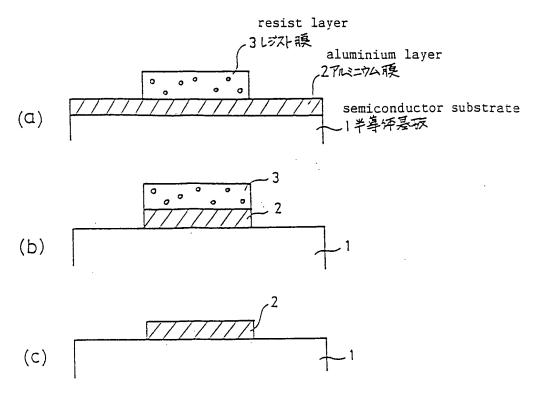


diagram indicating the principle of the present invention 京理図 第1図 Fig. 1



sectional view of the essential portion of an ashing apparatus to which the present invention is applied



processing sequence diagrams for patterning of aluminium wirings

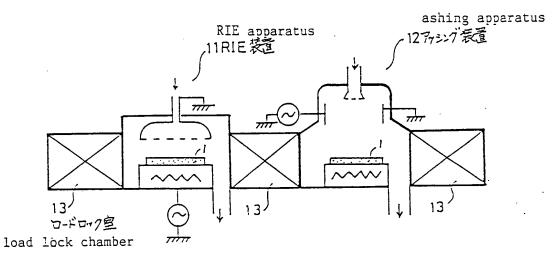


diagram indicating an automatic processing system

aluminium wiring
27ルミニウム配線

(Q)

HD

HD

Semiconductor substrate

(b)

diagram for explaining problems of the prior art 従来の向疑点を説明する図 第 5 図 Fig. 5